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	Application No.	Applicant(s)	
Nation of Allowahility	10/624,617	HART ET AL.	
Notice of Allowability	Examiner	Art Unit	
<u> </u>	James Cho	2819	
The MAILING DATE of this communication appearance All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED if or other appropriate comm IGHTS. This application is	n this application. If not included unication will be mailed in due course. TH	IIS tiative
1. \boxtimes This communication is responsive to <u>the application filed 7</u>	<u>'-23-2003</u> .		
2. A The allowed claim(s) is/are <u>1-28</u> .			
3. The drawings filed on 23 July 2003 are accepted by the Ex	kaminer.		
 4. Acknowledgment is made of a claim for foreign priority ur a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority do International Bureau (PCT Rule 17.2(a)). * Certified copies not received: 	e been received. e been received in Applicati	on No	ne
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		e a reply complying with the requirements	
5. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give			
6. CORRECTED DRAWINGS (as "replacement sheets") mus	st be submitted.		
(a) 🗌 including changes required by the Notice of Draftspers	son's Patent Drawing Revie	w (PTO-948) attached	
1) 🗌 hereto or 2) 🗍 to Paper No./Mail Date			
(b) including changes required by the attached Examiner's Paper No./Mail Date	s Amendment / Comment o	r in the Office action of	
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t	.84(c)) should be written on the header according to 37 C	he drawings in the front (not the back) of FR 1.121(d).	
7. DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT			
Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/C Paper No./Mail Date	6. Interview S Paper No 08), 7. Examiner's	Informal Patent Application (PTO-152) Summary (PTO-413), /Mail Date Amendment/Comment Statement of Reasons for Allowance	
of Biological Material	9. 🗌 Other	 .	
		James Cho Primary Examiner Art Unit: 2819	

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DETAILED ACTION

Allowable Subject Matter

1. Claims 1-28 are allowable over the prior art of record.

The following is an Examiner's Statement of Reasons for Allowance: Although, Rockett (US PAT No. 6,369,630) discloses a single event upset hardened integrated circuit, Rockett differs from the present claimed invention because, among other things, Rockett pertains to setting an asymmetric bi-stable latch to a known state upon activation of the supply voltage whereas the present claimed invention pertains to a method of implementing a user circuit in a programmable logic device. In the present claimed invention, based on the cost criteria a first or a second logical grouping are selected and a configuration data filed enabling a first level of well biasing for the first logical grouping and a second level of well biasing for the second logical grouping is generated, and a method of implementing a user circuit in a programmable logic device comprising a step of determining first set of transistors on a first non-critical path, designating the first set of transistors as non-critical path transistors and generating a configuration data file enabling negative well biasing of the non-critical path transistors.

Accordingly, one of ordinary skill in the art would not have been motivated to modify the teachings of Rockett to meet the claimed limitation as set forth in the present claimed invention.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably

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accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Patel et al. (US PAT No. 6,604,228) discloses a technique of fabricating integrated circuits having interfaces compatible with different operating voltage conditions.

Wong (US PAT No. 6,630,838) discloses a method for implementing dynamic burn-in testing using static test signals.

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to James Cho whose telephone number is 571-272-1802. The examiner can normally be reached on M-F 6:30 AM - 3:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mike Tokar can be reached on 571-272-1812. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should

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you have questions on access to the Private PAIR system, contact the Electronic

Business Center (EBC) at 866-217-9197 (toll-free).

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March 16, 2004